



THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Hashemi, et. al.

Serial No.: 09/878,815

Filed: June 11, 2001

For: **Structure and Method for Fabrication of
a Leadless Chip Carrier**

Art Unit: 2811

Examiner: Patricia M. Costanzo

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The items identified in this Information Disclosure Statement may or may not be "material" as defined in 37 C.F.R. § 1.56, and the submission thereof by Applicants is not to be construed as an admission that any such patent, publication or other information referred to is material or considered to be material (37 C.F.R. § 1.97(h)), or even qualifies as "prior art" under 35 U.S.C. § 102 with respect to the present invention unless specifically designated by Applicants as such. Identification of any reference or patent herein is not an admission, nor is it to be construed as an admission, that it was invented prior to the invention disclosed herein.

The filing of this Information Disclosure Statement is not to be construed to mean that a search has been made or that no other material information, as defined in 37 C.F.R. § 1.56, exists.

In accordance with 37 C.F.R. § 1.97 no fee is believed due at this time, since the Information Disclosure Statement is being filed before the mailing date of the first Office action on the merits, and since no item of information disclosed herein was known more than three months prior to the filing date of this statement. The Assistant Commissioner is hereby authorized to charge or credit Deposit Account Number 50-0731 for any deficiency or overpayment in the fees required for the filing of this Information Disclosure Statement, for which purpose a duplicate copy of this paper is also included.

Respectfully submitted,
Farjani & Farjani LLP

Dated: 6/6/02

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